

Recent IBM Patents

The following patents were recently issued by the countries in which the inventions were made. For U.S. patents, titles and names supplied to us by the U.S. Patent Office are reproduced exactly as they appear on the original published patent.

Federal Republic of Germany

2,811,318	H. Barsuhn, U. Olderdissen, and W. Schmidt	Storing Partial Words in Memory
2,964,971,608	K. Heuber and S. K. Wiedmann	Method and Circuit Arrangement for Controlling an Integrated Semiconductor Memory
2,965,000,808	R. Clemen, J. Gschwendtner, and W. Haus	Dynamic Semiconductor Memory Read/Write Access Circuit

France

9,096	J. L. Monrolin	Method and System for Synchronizing a TDMA Communication Network Comprising a Satellite Equipped with Several Directional Beams
9,557	R. Monrolin	Data Transmission Method and Devices for Practicing Said Method
11,737	J. P. Capuron	Semiconductor Integrated Circuit Layout and Method of Implementing the Same

Sweden

22,458	M. A. Enser	Hierarchical Computer System
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Switzerland

10,600	H. R. Schindler	Method and Apparatus for Transmission of Speech Signals
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United States

4,361,768	T. V. Rajeevakumar	Superconducting Soliton Devices
4,361,781	S. W. Depp and B. P. Piggin	Multiple Electron Beam Cathode Ray Tube
4,361,845	G. J. Smith	Device for Preventing the Contamination of Ink Jet Components
4,362,406	J. M. Choberka and D. K. Rex	Dot Matrix Print Head
4,362,486	G. T. Davis, E. H. Faulkner, A. S. Gasparri, R. A. Magee, L. P. Remsen, D. L. Saylor, and A. A. Stricker	Automatic Multilayer Ceramic (MLC) Screening Machine
4,362,596	B. H. Desilets and T. A. Gunther	Etch End Point Detector Using Gas Flow Changes
4,362,611	J. S. Logan, S. I. Petvai, and C. Rosu	Quadrupole R.F. Sputtering System Having an Anode/Cathode Shield and a Floating Target Shield
4,362,662	E. M. Engler, V. V. Patel, and R. R. Schumaker	Heterofulvalene Geminal Dithiolate Compounds and Their Selenium and Tellurium Analogs and a Method of Fabricating the Same
4,362,798	H. W. Anderson and R. B. Champ	Hydrazone and Pyrazoline or Acetosol Yellow Containing Charge Transport Layer, Photoconductor and Electrophotographic Process Using the Same
4,362,843	D. J. Bahr, M. Briska, and K. Schackert	Process for Producing a Polymerized, Heat-Resistant Lacquer
4,362,977	R. C. Evans, J. E. Griffith, M. M. Kutcher, and P. M. Will	Method and Apparatus for Calibrating a Robot to Compensate for Inaccuracy of the Robot
4,363,044	D. O. Castrodale and T. R. Fournier	Tray for Magnetic Disk Drive Machine
4,363,057	M. M. Siverling and S. E. Wheeler	Recirculating Filter Duct Design
4,363,093	M. I. Davis and D. T. W. Sze	Processor Intercommunication System
4,363,110	H. L. Kalter, H. N. Kotecha, and P. T. Patel	Non-Volatile Dynamic RAM Cell
4,363,124	F. J. Aichelmann, Jr.	Recirculating Loop Memory Array Tester
4,363,125	J. A. Brewer and J. A. Lowy	Memory Readback Check Method and Apparatus
4,363,549	L. C. Brown, J. L. Campbell, U. J. Fitts, and W. L. Gudgel	Electromechanically Operated Fuser Roll Closure
4,363,828	M. H. Brodsky and B. A. Scott	Method for Depositing Silicon Films and Related Materials by a Glow Discharge in a Disilane or Higher Order Silane Gas
4,363,919	E. M. Engler, V. V. Patel, and R. R. Schumaker	Heterofulvalene Geminal Dithiolate Compounds and Their Selenium and Tellurium Analogs and a Method of Fabricating the Same
4,364,024	K. Paetsch	Signature Presentation Method and Apparatus
4,364,074	R. R. Garnache, D. M. Kenney, and N. G. Thoma	V-MOS Device With Self-Aligned Multiple Electrodes
4,364,100	H. D. Edmonds and G. Markovits	Multi-Layered Metallized Silicon Matrix Substrate

- 4,364,166 B. L. Crowder and A. Reisman
 4,364,683 I. D. Schakib and K. Yosmali
 4,365,163 D. E. Davis, R. D. Moore, P. M. Ryan, and E. V. Weber
 4,365,235 E. C. Greanias and E. F. Yhap
 4,365,296 S. W. Ulmer
 4,365,317 T. R. Gheewala
 4,365,318 F. J. Aichelmann, Jr. and B. E. Bachman
 4,365,779 B. D. Bates and R. A. Haus
 4,366,493 N. Braslau, J. L. Freeouf, G. D. Pettit, H. S. Rupprecht, and J. M. Woodall
 4,366,518 W. W. Chow and D. S. Fields, Jr.
 4,366,537 A. R. Heller and W. S. Worley, Jr.
 4,366,540 N. C. Berglund and J. R. Burchfiel, Jr.
 4,366,613 S. Ogura and P. J. Tsang
 4,367,044 R. M. Booth, Jr. and C. A. Wasik
 4,367,052 D. J. Steger
 4,367,119 J. S. Logan, J. L. Mauer IV, L. B. Rothman, G. C. Schwartz, and C. L. Standley
 4,367,503 R. C. Treseder
 4,367,549 P. Vachee
 4,367,947 N. K. Arter, W. C. Dodt, and C. A. Farel
 4,368,220 J. M. Eldridge, M. H. Lee, and G. C. Schwartz
 4,368,466 J. D. Dwire
 4,368,513 D. Meltzer
 4,368,538 R. A. McCorkle
 4,368,977 N. K. Arter, M. D. Avritt, W. C. Dodt, and M. J. Herman
 4,369,072 P. E. Bakeman, Jr., A. G. Fortino, H. J. Geipel, Jr., J. P. Kasold, and R. M. Quinn
 4,369,154 W. E. Dougherty
 4,369,271 D. J. Bahr and M. Briska
 4,369,396 I. D. Judd
 4,369,463 D. Anastassiou
 4,369,501 C. J. Brown and L. D. Provazek
 4,370,071 A. B. Habich and R. E. Hunt
 4,370,197 O. R. Abolafia, J. G. Ameen, and G. V. Elmore
 4,370,554 H. Bohlen, J. Greschner, W. Kulcke, and P. Nehmiz
 4,370,641 S. Kantor, R. A. Maddox, and P. S. Yosim
 4,370,645 W. C. Cason, W. A. Kuecker, and S. D. Stratton
 4,370,651 G. A. Reible, Jr.
 4,370,732 P. M. Kogge
 4,370,746 R. E. Jones and D. H. Wood
 4,371,157 R. E. Hunt and W. M. Jenkins
 4,371,273 A. H. Kendall, J. W. Mitchell, and C. J. Sambucetti
 4,371,565 A. I. Baise, J. M. Burns, and H. S. Sachdev
 4,371,857 A. Bohg and K. Hartmann
 4,371,902 D. W. Baxter, J. L. Evjen, Jr., W. C. Hoveland, R. A. Roble, D. R. Smith, and J. H. Wirz
 4,371,929 J. J. Brann, C. S. Freer, Jr., and W. W. Jensen
 4,371,932 J. M. Dinwiddie, Jr., B. J. Freeman, T. Jackson, and W. L. Zipoy
 Semiconductor Integrated Circuit Interconnections
 Paper Feed Roller Assembly for a Typewriter or Printer
 Pattern Inspection Tool—Method and Apparatus
 Chinese/Kanji On-Line Recognition System
 System for Controlling Duration of the Time Interval Between Blocks of Data in a Computer-to-Computer Communication System
 Superconductive Latch Circuit
 Two Speed Recirculating Memory System Using Partially Good Components
 Tilt and Rotate Apparatus for a Display Monitor
 Semiconductor Ballistic Transport Device
 Multi-Track Head Assembly
 Authorization Mechanism for Transfer of Program Control or Data Between Different Address Spaces Having Different Storage Protect Keys
 Cycle Control for a Microprocessor with Multi-Speed Control Stores
 Method of Fabricating an MOS Dynamic RAM with Lightly Doped Drain
 Situ Rate and Depth Monitor for Silicon Etching
 Flat Rate Spring Particularly Adapted for Typewriter Cartridges
 Planar Multi-Level Metal Process With Built-In Etch Stop
 Fermetically Sealed Disk File
 Method and Apparatus for Multiplexing a Data Signal and Secondary Signals
 Document Feeder for Moving Bed Machines such as Copiers
 Passivation of RIE Patterned Al-Based Alloy Films by Etching to Remove Contaminants and Surface Oxide Followed by Oxidation
 Display Refresh Memory with Variable Line Start Addressing
 Partial Roll Mode Transfer for Cyclic Bulk Memory
 Spot Focus Flash X-Ray Source
 Document Ejector Apparatus and Method Useful for Copiers
 Method for Forming IGFET Devices Having Improved Drain Voltage Characteristics
 Process for Producing Smoother Ceramic Surfaces
 Lacquer for Record Carriers, and Process for its Production
 Color Cathode-Ray Tube Apparatus with Shadow Mask
 Gray Scale Image Data Compression with Code Words a Function of Image History
 Dual Cycle Data Detection System and Method for Bubble Memories
 Print Package
 Process for Etching Chrome
 Alignment System for Particle Beam Lithography
 Electronic Control System
 Ghost Cursor in Display All Codes Mode
 Advanced Plasma Panel Technology
 Skewed Matrix Address Generator
 Memory Address Selector
 Compact Envelope Handling Device
 Electrochemical Printhead
 Process for Adhering an Organic Resin to a Substrate by Means of Plasma Polymerized Phosphines
 Electromagnetically Operable RAM Actuator in Particular for Impact Printers
 Disk Initialization Method
 Multiprocessor System with High Density Memory Set Architecture Including Partitionable Cache Store Interface to Shared Disk Drive Memory
 I/O Controller for Transferring Data Between a Host Processor and Multiple I/O Units